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10/828,492	04/21/2004	327	2826	60188-820

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TD

** CONTINUING DATA *****

This application is a DIV of 10/187,269 07/02/2002 PAT 6,734,549

TD

** FOREIGN APPLICATIONS *****

JAPAN 2001-201950 07/03/2001

TD

** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

06/29/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> TD Initials	JAPAN	25	8	4

ADDRESS

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TITLE

Semiconductor wiring substrate, semiconductor device, method for testing semiconductor device, and method for mounting semiconductor device

FILING FEE RECEIVED 856	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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